



The RISC-V Microconference is on Break

7:00 - 7:05: Introduction [Palmer Dabbelt]
7:05 - 7:45: The RISC-V Platform Specification [Atish Patra]
7:45 - 8:15: Next Generation RISC-V Interrupt Support [Anup Patel]
8:15 - 8:30: BREAK
8:30 - 9:00: ACPI for RISC-V [Sunil V L]
9:00 - 9:45: What's the Problem with D1 Linux Upstream [Guo Ren]
9:45 - 10:00: BREAK
10:00 - 10:30: Puzzle for RISC-V ifunc [Kito Cheng]
10:30 - 11:00: ... Code Generation for RISC-V [Phillip Tomsich]
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